



Fig. 1

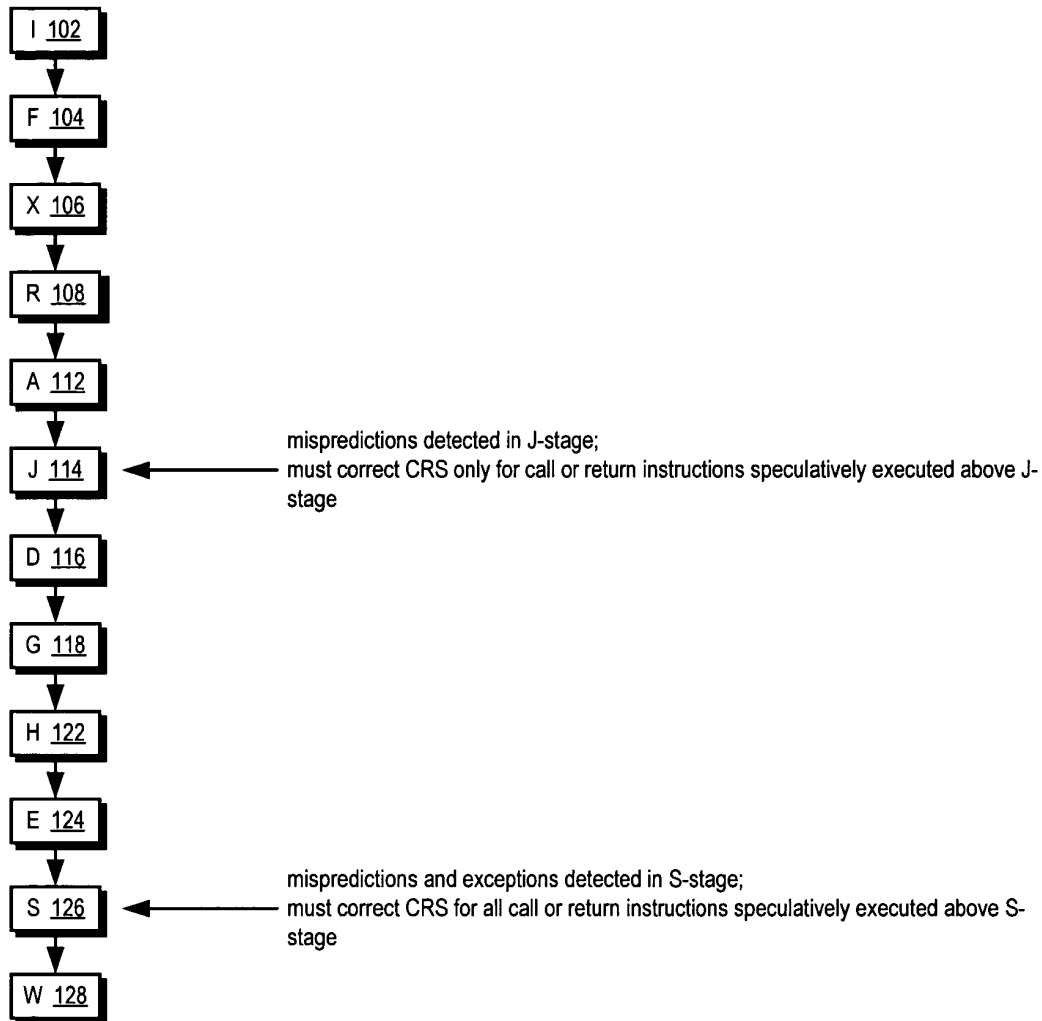
Microprocessor with Call/Return Stack100


Fig. 2

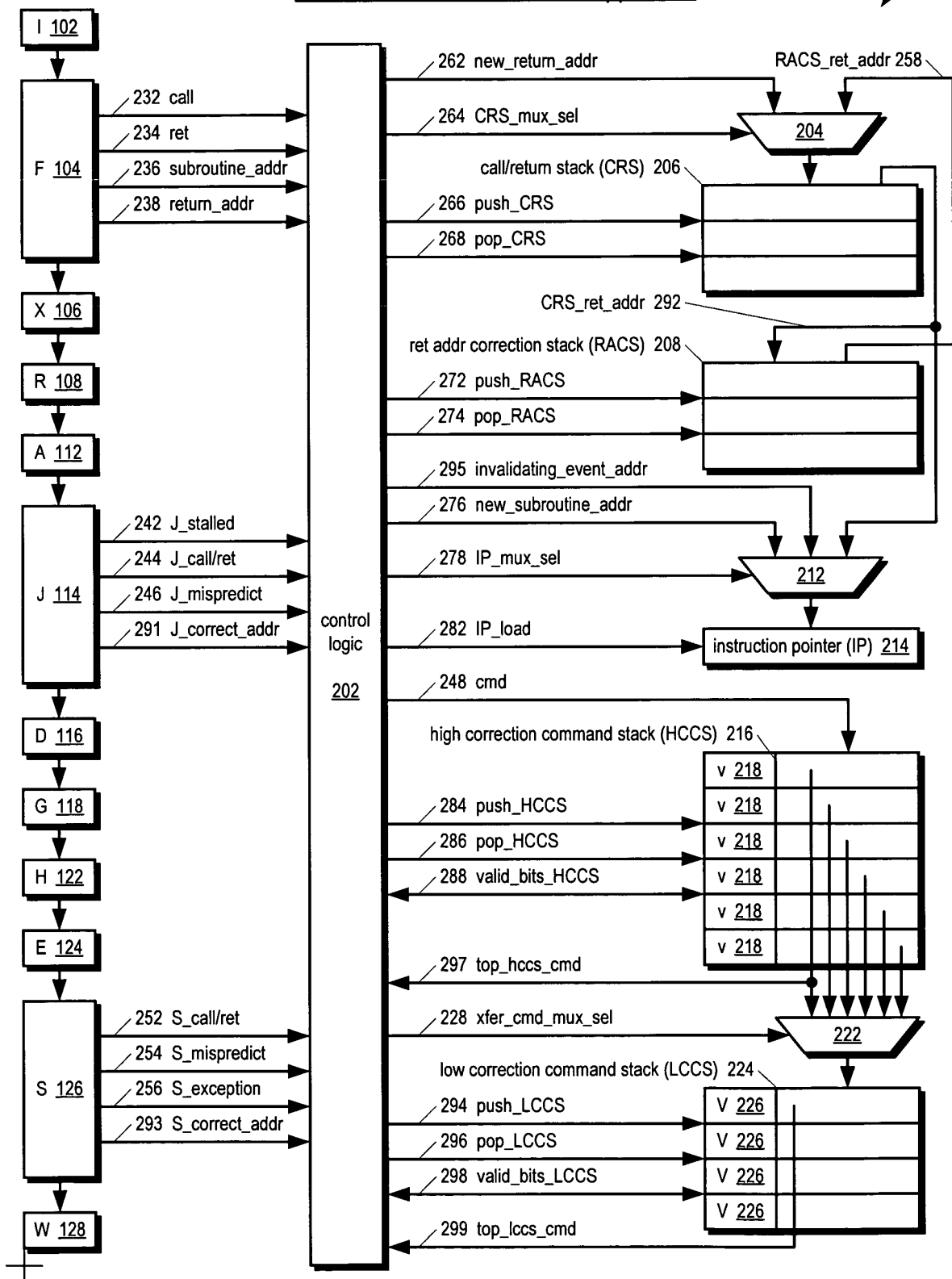
Call/Return Stack Correction Apparatus



Fig. 3

Call/Return Stack Correction Apparatus Operation

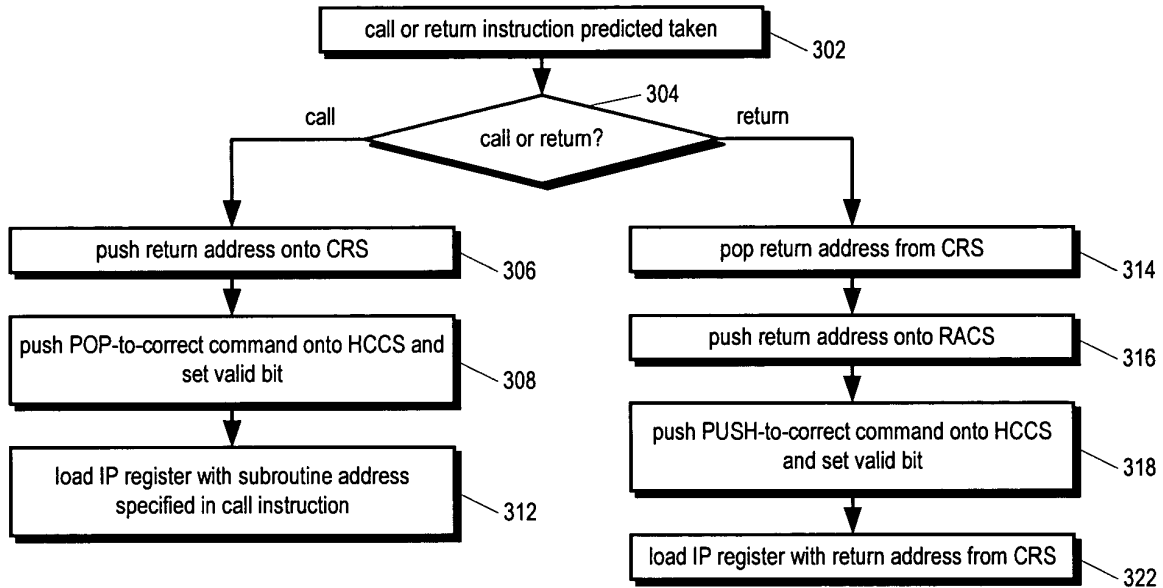


Fig. 4

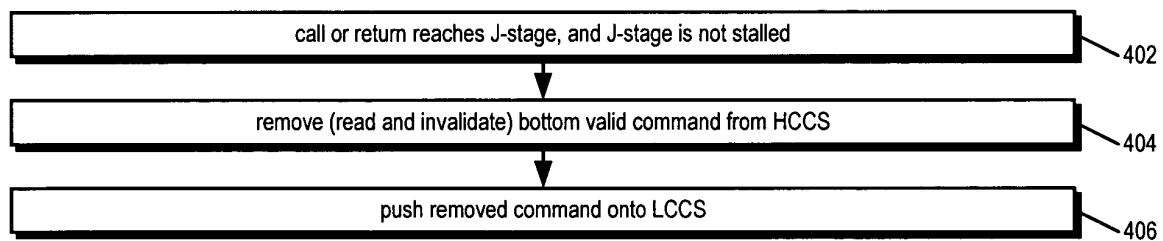


Fig. 5

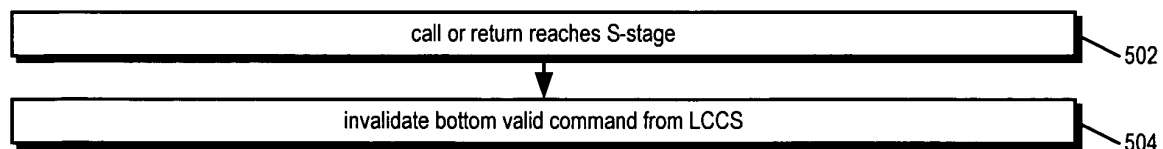


Fig. 6

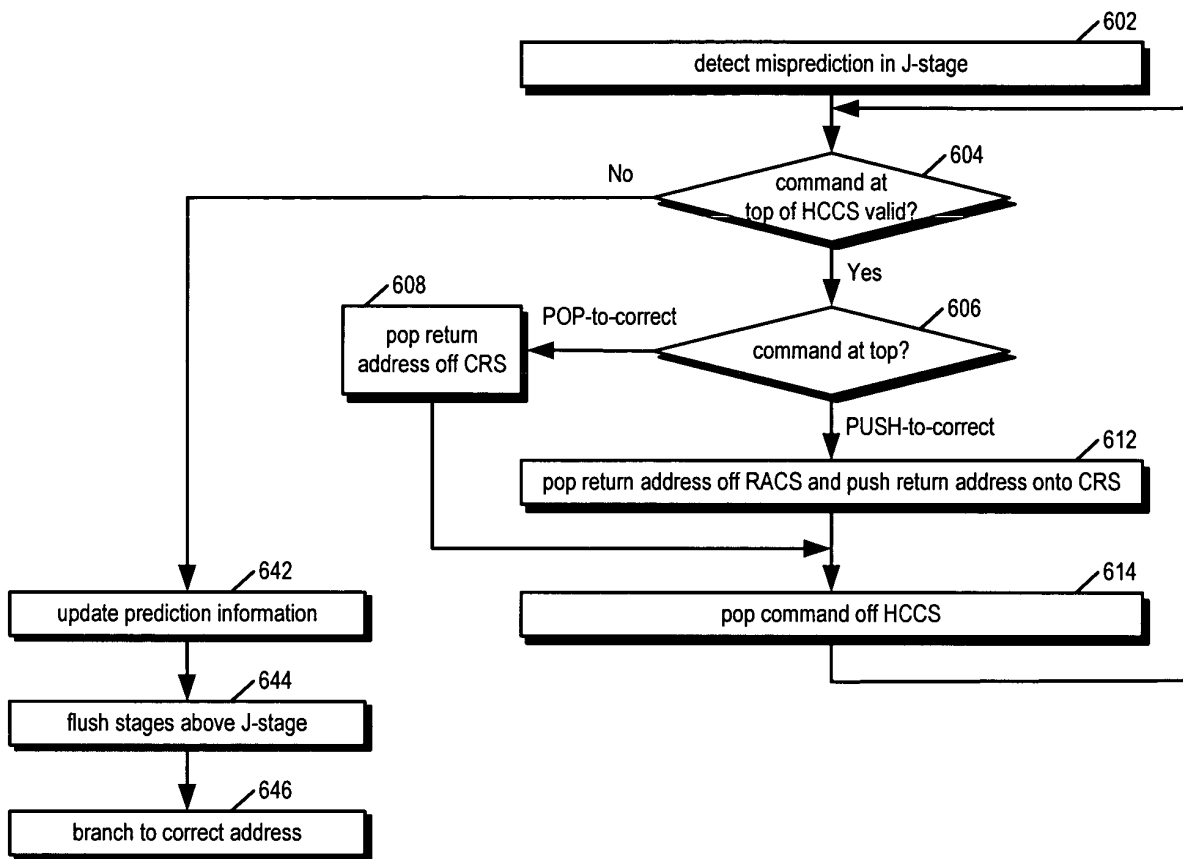
Call/Return Stack Correction Apparatus Operation



Fig. 7

Call/Return Stack Correction Apparatus Operation